

ABSTRACT OF THE DISCLOSURE

An apparatus comprising a transceiver circuit. The transceiver circuit comprises a plurality of bus input/outputs (I/Os). The transceiver circuit may be configured to directly couple (i) an analog signal to the bus I/Os when the bus I/Os are 5 in a first state and (ii) a plurality of first digital signals to the bus I/Os when the bus I/Os are in a second state.

1 2 3 4 5 6 7 8 9 0